

In re Patent Application of:
LEAMING
Serial No. 10/829,008
Filing Date: April 21, 2004

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In the Claims:

This listing of claims replaces all prior versions and listing of claims in the application.

1. (Currently amended) An integrated circuit for a smart card comprising:

at least one data terminal for providing communications with a host device over a system bus; and

a processor for

providing an attachment signal on the at least one data terminal for recognition by the host device,

cooperating with the host device to perform an enumeration based upon at least one default descriptor, and

~~based upon a system utilization metric exceeding a threshold,~~ selectively removing the attachment signal from the at least one data terminal and thereafter again providing the attachment signal on said at least one data terminal and cooperating with the host device to perform a new enumeration based upon at least one alternate descriptor, ~~the system utilization metric based upon a number of other devices communicating with the host device~~ based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus.

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2. (Original) The integrated circuit of Claim 1 further comprising at least one power terminal connected to said processor, and wherein said processor receives power via said at least one power terminal during removal of the attachment signal.

Claims 3 and 4 (cancelled).

5. (Original) The integrated circuit of Claim 1 wherein said processor monitors communications with the host device during removal of the attachment signal.

6. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one device descriptor.

7. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

8. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one interface descriptor.

9. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

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10. (Original) The integrated circuit of Claim 1 wherein said at least one data terminal comprises first and second data terminals for differential data signals.

11. (Original) The integrated circuit of Claim 1 further comprising a USB transceiver connected between said processor and said at least one data terminal.

12. (Currently amended) A smart card comprising:
a smart card body; and
an integrated circuit carried by said smart card body
and comprising

at least one data terminal for providing
communications with a host device over a system bus,
and

a processor for

providing an attachment signal on the at
least one data terminal for recognition by the
host device,

cooperating with the host device to perform
an enumeration based upon at least one default
descriptor, and

~~based upon a system utilization metric~~
~~exceeding a threshold~~, selectively removing the
attachment signal from the at least one data

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terminal and thereafter again providing the attachment signal on said at least one data terminal and cooperating with the host device to perform a new enumeration based upon at least one alternate descriptor, ~~the system utilization metric based upon a number of other devices communicating with the host device~~ based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus.

13. (Original) The smart card of Claim 12 wherein said integrated circuit further comprises at least one power terminal connected to said processor, and wherein said processor receives power via said at least one power terminal during removal of the attachment signal.

Claims 14 and 15 (cancelled).

16. (Original) The smart card of Claim 12 wherein said processor monitors communications with the host device during removal of the attachment signal.

17. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one device descriptor.

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18. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

19. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one interface descriptor.

20. (Original) The smart card of Claim 12 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

21. (Original) The smart card of Claim 12 wherein said at least one data terminal comprises first and second data terminals for differential data signals.

22. (Original) The smart card of Claim 12 further comprising a USB transceiver connected between said processor and said at least one data terminal.

23. (Currently amended) A smart card system comprising:
a host device and associated system bus;
a smart card adapter connected to said host device via said system bus; and

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a smart card to be read by said smart card adapter and comprising a smart card body and an integrated circuit carried by said smart card body, said integrated circuit comprising

at least one data terminal for providing communications with a host device over said system bus, and

a processor for

providing an attachment signal on the at least one data terminal for recognition by said host device,

cooperating with said host device to perform an enumeration based upon at least one default

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